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प्लाज़्मा अनुसंधान संस्थान INSTITUTE FOR PLASMA RESEARCH

परमाणु ऊर्जा विभाग, भारत सरकार का एक सहायता प्राप्त



संस्थान

An Aided Institute of Department of Atomic Energy, Government of India

इन्दिरा पुल के पास, भाट, गांधीनगर – 382 428 भारत दूरभाष: (079) 2396 2020/2021/2028 फैक्स: 91-079-23962277 वेब: <u>www.ipr.res.in</u> NEAR INDIRA BRIDGE, BHAT DIST. GANDHINAGAR - 382 428 (INDIA) Phone: (079) 2396 2020/2021/2028 Fax : 91-079-23962277 Web : <u>www.ipr.res.in</u>

ENQUIRY

ENQUIRY NO Date : IPR/EQL/18-19/287 : 27-11-2018

Due on : 20-12-2018 by 1:00 PM IST

Please send your offer in sealed envelope specifying Enquiry No, Date & Due Date, ALONG WITH your credentials for the following items:

Important Note:

Please note that e-mail quotations are not acceptable however you may send your queries (if any) to <u>localpurchase@ipr.res.in</u>

Please ensure your sealed quotation reaches this office not later than above mentioned due date and time.

Kindly go through the following documents properly before quoting which are available on the IPR web portal i.e., <u>http://www.ipr.res.in/documents/tender_terms.html</u> / attached herewith.

Instructions to the bidders & Terms and conditions (refer Form No: IPR-LP-01.V4)
 Bidding format

GST for Goods and Services (IGST/CGST/SGST TAX BENEFITS): Please refer clause no: 8 of Form No: IPR-LP-01.V4

QUOTATION SHOULD BE ADDRESSED TO PURCHASE OFFICER ONLY

Sr No	Description	Quantity
1	Fiber optics transmitter : IC HFBR-1414Z or Equivalent	20.0 Nos.
2	Fiber optics receiver : IC AFBR-2418Z or Equivalent	20.0 Nos.
3	Quadruple 2-input positive Nand gates; part number: CD74ACT00E or equivalent	50.0 Nos.
4	Quadruple Bus Buffer Gates With 3-State Outputs; part number: SN74HC125N or equivalent	50.0 Nos.
Note:	TDS as per CGST Act: As per provisions of section No. 51 of the CGST Act 2017, TDS @2% (IGST 2% or CGST 1% and SGST 1%) will be deducted while making payment to the suppliers where total value of orders/contracts/work orders exceeds Rs. 2.5 lakhs, in the event of order in Indian Rupees. Necessary TDS	

Certificate will be issued to the supplier after TDS deduction. Encl: As per attachment.

Sd/-

Mr. D. Ramesh Purchase Officer-II

Information to Vendors: We are working towards a single platform for our future requirement. Hence, please refer IPR website i.e, http://www.ipr.res.in/documents/tenderseng.html for our future requirement.

	Technical Complinace form	n for Item-1
Sr.No	IPR Requirements	Vendor's specification
1	125 megabaud versatile link	
2	Horizontal Housing	
3	Easily interfaced with standard TTL and CMOS logic.	anot installos a by
4	Operating Temp: -40°C to 85°C	
5	Peak Forward I/P Current :200mA Max	Ø
6	DC Forward I/P current : 100mA Max	The constraint is the statement
7	Reverse i/p Break down Voltage: 3.8V at Ifdc= -100µA.	
8	Peak Emission Wavelength : 820nm	
9	Forward Voltage : 1.48 V to 2.09 V at 60mA	manufacture and a state and
10	Diode Capacitance: 55 pF	
11	Rise Time:4 ns	
12	Fall Time: 4 ns	
13	Part No: HFBR-1414Z Or equivalent.	
14	Quntity: 20 Nos.	1

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Sr.No	Technical Compliance form	Vendor's Specification
1	50 megabaud versatile link	vendor's specification
2	Horizontal Housing	
3	Operating Temp: -40°C to 85°C	
4	Supply Voltage (Vcc) : -0.3V to 5.5V	
5	Maximum DC Output Current: 10 mA	
6	Optical Spectrum Range: 820 nm	
7	High Level Output Voltage: VCC max, $RL=2k\Omega$	
8	Low Level Output Voltage: 0.4 V , $RL=2k\Omega$	
9	Output rise time: 5 ns	
10	Output fall time: 5 ns	
11	Propagation Delay: 40 ns	
12	Part No: AFBR-2418Z Or equivalent.	
13	Quntity:20 Nos.	

Technical Compliance form for Item-3		
Sr.No	IPR Requirements	Vendor's Specification
1	Part Number: CD74ACT00E(Quadruple 2- input positive Nand gates)	
2	Package Type: 14 PIN-PDIP	
3	Input are TTL-voltage compatible	
4	Operating Tempearture: -55°C to +125°C	
5	Supply Voltage: 5.5 Vdc	י. - אילי אין אילצעל אש כאברובי קאריבציאי, אשר אי איזיראישטיר ואין לאשראלי לא איז איני איזי איז איזי איזי איזי איז - איזי איזי איזי איזי איזי איזי איזי איז
6	Input voltage : 0 to VCC	
	Output voltage : 0 to VCC	

	 Switching characteristics 	
8	-FPLH: 10.8 ns or better	
And the second secon	- TPHL: 13.2ns or better	Supramianel and
9	Quntity: 50 Nos.	and a second

Sr.No	Technical Compliance form IPR Requirements	Vendor's Specification
1	Part Number: SN74HC125N (Quadruple Bus Buffer Gates With 3-State Outputs)	
2	Package Type:14 PIN-PDIP	
3	Supply Voltage: 5 Vdc	
4	High-level input voltage : 3.15 VDC	
5	Low-level input voltage : 1.35 VDC	a de caracter anna de caracter a construction de construction de construction de construction de construction d
6	Operating Tempearture:40°C to +85°C	
1000	Input voltage : 0 to VCC	
	Output voltage : 0 to VCC	
	Quntity: 50 Nos.	

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