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प्लाज़्मा अनुसंधान संस्थान
INSTITUTE FOR PLASMA RESEARCH
परमाणु ऊर्जा विभाग, भारत सरकार का एक सहायता



प्राप्त संस्थान
An Aided Institute of Department of Atomic Energy,
Government of India

इन्दिरा पुल के पास, भट, गांधीनगर - 382 428 भारत

दूरभाष: (079) 2396 2020/2021/2028

फैक्स: 91-079-23962277

वेब: www.ipr.res.in

NEAR INDIRA BRIDGE, BHAT

DIST. GANDHINAGAR - 382 428 (INDIA)

Phone: (079) 2396 2020/2021/2028

Fax : 91-079-23962277

Web : www.ipr.res.in

ENQUIRY

ENQUIRY NO : IPR/EQL/19-20/146

Date : 09-07-2019

Due on : 01-08-2019 by 1:00 PM IST

Please send your offer in sealed envelope specifying Enquiry No, Date & Due Date,
ALONG WITH your credentials for the following items:

Important Note:

Please note that e-mail quotations are not acceptable however you may send your
queries (if any) to localpurchase@ipr.res.in

Please ensure your sealed quotation reaches this office not later than above mentioned
due date and time.

Kindly go through the following documents properly before quoting which are available on
the IPR web portal i.e., http://www.ipr.res.in/documents/tender_terms.html / attached
herewith.

- 1) Instructions to the bidders & Terms and conditions (refer Form No: **IPR-LP-01.V4**)
- 2) Bidding format

GST for Goods and Services (IGST/CGST/SGST TAX BENEFITS): Please refer **clause no: 8** of Form No: **IPR-LP-01.V4**

QUOTATION SHOULD BE ADDRESSED TO PURCHASE OFFICER ONLY

| Sr No | Description | Quantity |
|-------|----------------------------------|-----------|
| 1 | 4 inch prime grade silicon wafer | 25.0 Nos. |
| 2 | 6 inch prime grade silicon wafer | 25.0 Nos. |

Note: Note : (1) Unsigned quotations are not acceptable.
Quotation should be submitted duly signed on ALL PAGES
invariably

(2) TDS as per CGST Act : As per provisions of section No. 51 of the CGST Act 2017, TDS @ 2% (IGST 2% or CGST 1% and SGST 1%) will be deducted while making payment to the suppliers where total value of orders/contracts/work orders exceeds Rs. 2.5 Lakhs, in the event of order in Indian Rupees
delivery time:- 04 months

Encl: As Per Attachment

Sd/-

Mr. D. Ramesh
Purchase Officer-II

Information to Vendors: We are working towards a single platform for our future requirement. Hence, please refer IPR website i.e, <http://www.ipr.res.in/documents/tenderseng.html> for our future requirement.

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Compliance Sheet

| Sr. No. | IPR Specification | IPR Requirement | Vendor |
|---------|---|--|--------|
| 1 | 4 Inch Silicon wafer a) Grade b) Diameter c) Thickness d) Surface e) Orientation f) Type | a) Prime b) 4 inch c) 400-600 μm d) Single side polished e) <100> f) Intrinsic/Undoped | |
| 2 | 6 Inch Silicon wafer a) Grade b) Diameter c) Thickness d) Surface e) Orientation f) Type | a) Prime b) 6 inch c) 500-700 μm d) Single side polished e) <100> f) Intrinsic/Undoped | |

Specification of Silicon Wafer

4 inch silicon wafer:

Grade : Prime
Diameter : 4 Inch
Thickness : 400-600 μm
Surface : Single side polished
Orientation : $\langle 100 \rangle$
Type : Intrinsic/Undoped

6 inch silicon wafer:

Grade : Prime
Diameter : 6 Inch
Thickness : 500-700 μm
Surface : Single side polished
Orientation : $\langle 100 \rangle$
Type : Intrinsic/Undoped